



VACANCY BULLETIN

**Tshwane University
of Technology**

We empower people

**PART-TIME LECTURERS x16
DEPARTMENT OF COMPUTER SYSTEMS ENGINEERING
FACULTY OF ICT
SOSHANGUVE SOUTH CAMPUS
Ref: 26/G47**

The Department of Computer Systems Engineering, Faculty of ICT invites applications for 16 part-time lecturers to teach the following Diploma and Advanced Diploma subject(s):

Diploma level

EL1F05D Electronics 115 (Extended) (1 candidate)
LOD216D Logic Design 226 (3 candidates)
OPS216D Operating Systems 226 (2 candidates)
CATF16D Computer Architecture 125R (4 candidates)
ORS216D Operating Systems (5 candidates)

Advanced Diploma level

IIS117V Intelligent Industrial Systems (1 candidate)

NB: Indicate the module(s) you are applying for

INSTRUCTIONS: Applicants are required to submit, together with the CVs, a fully completed standardized application form available from the university intranet as well as recently certified copies of qualifications and Identity Document. Failure to comply with this instruction will disqualify the candidate. Candidates may be subjected to appropriate psychometric testing and other selection instruments.

Key performance areas/Core functions:

- Preparing all lecture materials, assessments, and related documents.
- Presenting lectures and assessing students in the department.
- Performing administration of marks and other related administrative duties under the subject head, sectional head and HOD supervision.
- Collate and submit complete course files (including lesson plans, assessments, moderation reports, and student performance records) before and after each semester.
- Integrate ECSA Graduate Attributes into curriculum delivery, assessment design, and student development.
- Participate in ECSA accreditation preparation, including evidence collection, documentation, and quality assurance activities.
- Undertake other related academic or administrative tasks assigned by the HOD.

Women and people with disabilities are encouraged to apply.





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- The appointments are for the TUT contact period for the 2nd semester of 2026.
- Successful applicants only receive a part-time salary according to TUT policy.
- No additional remuneration for travelling, relocation, etc.

Minimum requirements:

Academic qualifications:

- A Master's degree in Computer Systems Engineering / Intelligent Industrial Systems / Electrical Engineering / Mechanical Engineering with adequate knowledge in the required subjects.
- Postgraduate Diploma/Honours in Computer Systems Engineering /Intelligent Industrial Systems / Electrical Engineering / Mechanical Engineering with adequate knowledge in the required subjects.

Please note that candidates with a Postgraduate Diploma will only be considered if there are not enough qualified Master's candidates available.

Experience:

- At least 3 years of relevant teaching experience and/or 3 years of experience in industry.

Additional Requirements:

- Must attend the academic orientation programme designed for a part-time lecturer.
- Must attend Departmental Curriculum review

Registration with professional body (if applicable):

- Registration with ECSA is recommended

Postgraduate Students:

- Supervisor's progress report should be attached together with the application.

Send your CV, application form and supporting documents to:

https://docs.google.com/forms/d/e/1FAIpQLSfqABrlsrrA8-t_QBGm7RXUWAD3TccfKKryMxlfCB_ekoVIZQ/viewform?usp=dialog

Closing Date : 19 June 2026

Enquiries : Prof C Du DuC@tut.ac.za

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NB: If we have not responded within a month from the closing date, you should regard your application as unsuccessful. Correspondence will only be entered into with short-listed candidates. **The University reserves the right not to make an appointment.** It is the intention of the University to promote representativity in respect of race, gender and disability through the filling of this post.

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